Docket No.: 42P18068

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Re the Application of:

VASUDEVAN SRINIVASAN, ET AL.

Application No.: 10/750,395

Filed: December 30, 2003

For: Method and Apparatus for Determining

Processor Utilization

Art Group: TBA

Examiner: TBA

INFORMATION DISCLOSURE STATEMENT UNDER 37 C.F.R. §1.97

Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

In accordance with the duty of disclosure, enclosed is a copy of IDS Citation Form PTO/SB/08 or PTO-1449, together with copies of the documents cited on that form, except for copies not required to be submitted (e.g., copies of U.S. patents and U.S. published patent applications need not be enclosed for applications filed after June 30, 2003). This IDS and IDS Citation Form are being submitted before the mailing of a first Office Action. It is respectfully requested that the cited references be considered and that the enclosed copy of PTO/SB/08 be initialed by the Examiner to indicate such consideration and a copy thereof returned to applicant(s).

The submission of this Information Disclosure Statement is not to be construed as a representation that a search has been made in the subject application and is not to be construed as an admission that the information cited in this statement is material to patentability.

Please charge any fees due to Deposit Account 02-2666. A duplicate copy of the Fee Transmittal (PTO/SB/17) is enclosed for this purpose.

Respectfully submitted,

BLAKELY, SOKOLOFF, TAYLOR & ZAFMAN LLP

Date: July 01, 2004

Chui-Kiu Teresa Wong, Reg. No. 48,042

12400 Wilshire Boulevard, 7th Floor Los Angeles, CA 90025 Telephone: (408) 720-8300

I hereby certify that this correspondence is being deposited with the United States Postal Service on the date shown below with sufficient postage as first class mail in an envelope addressed to: Mail Stop Missing Parts, Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.

Esther L. Campbell

Data



INFORMATION DISCLOSURE STATEMENT BY APPLICANT

Sheet 1 of 1

Complete if Known				
Application Number	10/750,395			
Filing Date	December 30, 2003			
First Named Inventor	Vasudevan Srinivasan			
Art Unit	TBA			
Examiner Name	TBA			
Attorney Docket Number	42P18068			

NON PATENT LITERATURE DOCUMENTS					
Examiner Initials*	Cite No.¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T²		
		"Advanced Configuration and Power Interface Specification", Revision 2.0c, June 30 2004, Compaq/Intel/Microsoft/Phoenix/Toshiba, pages 1-488.			

Examiner	Date	
Signature	Considered	

^{*}Examiner: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication.

^{&#}x27;Applicant's unique citation designation number. 'Applicant is to place a check mark here if English language Translation is attached.